TTM4205: Technical Essay Fall 2024

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Assignment

This is one out of three assignments in the course TTM4205 Secure Cryptographic Implementations (ttm4205.iik.ntnu.no) during fall semester of 2024.

This assignment is to write a *technical essay* and give a *presentation* about a scientific topic related to the content given in the course description: either a topic not covered by the lectures or a topic from the lectures more in-depth. It will be joint work in groups of two or three, and the essay should be *roughly* 8 *to* 10 *pages* long, in addition to references. The topic, scope, and group must be *approved* by the staff.

- Cheating at NTNU: i.ntnu.no/wiki/-/wiki/English/Cheating+on+exam
- Writing tips at NTNU: i.ntnu.no/en/academic-writing/write-academically
- Bibliographic references to cryptology papers: publish.iacr.org/cryptobib
- CS paper: froihofer.net/students/how-to-write-a-computer-science-paper
- NTNU style guide: ntnu.edu/english-matters/ntnu-english-style-guide

All essays and presentation slides must be written in LaTeX, and we provide mandatory templates to be used at overleaf.com/read/nhcnrbnwzmcw (essay) and overleaf.com/read/zjqxggmjnzqp (presentation).

This assignment counts for at most 40 points, based on the following criteria: scientific correctness, quality of writing, the structure of the essay, presentation (figures/tables), referencing, relevant and consistent background material, clear and detailed main section(s), and justification of conclusions.

The topic must be approved by November 1st, but we recommend starting earlier. If you want the staff to provide feedback on your essay, you can submit a draft by November 22nd and get a response by December 6th. Oral presentations will be on November 19th or 22nd, to be scheduled later.

Submission deadline: December 20th at 23:59 in Ovsys2.

Suggested Topics

We suggest some relevant topics for the technical essay below, but you can also suggest your own. In the former case, you need to detail the scope of the essay yourself, and we allow for at most two groups working on the same high-level topic. In the latter case, you are expected to provide a (preliminary) title and scope, in addition to at least two references.

1 Cryptographic Fuzzing and Static Analysis

It is hard to verify if a given piece of cryptographic code is securely implemented or not. Vulnerabilities include side-channel leakage, lacking API checks, and correctness errors. One possible solution to detect these mistakes is cryptographic fuzzing [Som16]; see also github.com/kudelskisecurity/cdf. Furthermore, static analysis can be used to discover the wrong usage of randomness or cryptographic algorithms [LJL+22].

2 Formally Verified Cryptographic Code

While cryptographic fuzzing and static analysis are excellent approaches to finding vulnerabilities, they are reactive solutions that require much work after the code is written. A more proactive approach is only to allow correct and secure code to be written in the first place by disallowing insecure algorithms, automatically generating code [EPG+20], and using languages that do not compile if certain functions or operators are used [ZBPB17].

The talk by Filippo Valsorda on the design of the Go Crypto Library at infoq.com/presentations/go-crypto-library and the blog post by Microsoft on Project Everest at microsoft.com/en-us/research/blog/project-everest-reaching-greater-heights-in-internet-communication-security discuss this.

3 Vulnerabilities in Threshold Signatures

Lindell published a threshold signature scheme [Lin21] based on the Elliptic Curve Digital Signature Algorithm (ECDSA). This scheme was later implemented and used in practice, and, while the construction was theoretically secure, the implementations contained bugs that allowed an attacker to extract the secret key [AS20]. See also the report available at fireblocks.com/blog/lindell17-abort-vulnerability-technical-report. There are some more recent attacks on threshold ECDSA by Makriyannis, Yomtov and Galansky [MYG23] on similar schemes, presented at a NIST workshop: csrc.nist.gov/presentations/2023/mpts2023-day2-talk-attack-threshold-ecdsa-wallets.

4 Degenerate Edwards Curve Attacks

We get into trouble if we do not verify that points $P = (x, y)$ are on the (Weierstrass) elliptic curve $E(\mathbb{F}_p) : y^2 = x^3 + a \cdot x + b$ [ABM+02] (see; Weekly Problems). Furthermore, the addition formulas are not complete, which means that the way we compute the addition of two points P and Q on $E(\mathbb{F}_p)$ depends on the input. This makes implementation more complicated to get correct and enforces complex side-channel protection measurements since the difference in addition method may leak secret key data.

Edwards curves $Ed(\mathbb{F}_p) : y^2 - x^2 = 1 + d \cdot x^2 \cdot y^2$ (simplified) were introduced to solve these issues, leading to the more efficient and secure signature scheme EdDSA [BDL+11], which was later standardized by the Internet Research Task Force (IRTF) at datatracker.ietf.org/doc/html/rfc8032 (also including an implementation in Python). However, also these curves are vulnerable to specially crafted input points as showed by Neves and Tibouchi [NT16].

5 SCA Against Post-Quantum Cryptography

NIST is currently standardizing post-quantum cryptography; see overview at csrc.nist.gov/projects/post-quantum-cryptography/selected-algorithms-2022, and has chosen the key-encapsulation mechanism CRYSTALS Kyber and the digital signatures CRYSTALS Dilithium, Falcon, and SPHINCS+. While theoretically secure, there has been an effort to attack and protect these schemes against side-channel attacks; see, e.g., [MGTF19] for an analysis of rejection sampling, number-theoretic transforms, and polynomial multiplications in Dilithium.

NIST recently announced a new call for additional signature schemes, see csrc.nist.gov/projects/pqc-dig-sig/round-1-additional-signatures, and received 40 candidates. The lattice-based Raccoon scheme [dPEK+23, dPPRS23] is resistant against side-channel attacks, but no one has yet analyzed the other schemes for "side-channel friendliness".

6 More Advanced SCA with ChipWhisperer

The most common way to protect an implementation against side-channel attacks is through masking; however, this does not protect against glitching [MPO05], and many works have studied how to prove schemes secure against such attacks in the so-called probing model [MMSS19].

Conduct a similar experiment as the "Voltage (VCC) Glitching Raspberry Pi 3 Model B+ with ChipWhisperer-Lite" attack as shown by Colin O'Flynn at youtu.be/dVkCNiM0PL8. We will provide you with a Raspberry Pi 4. Then, use this knowledge to glitch a password checker, an RSA implementation, and/or some other cryptographic scheme.

References

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